	Application No.	Applicant(s)	
Notice of Allewshility	10/762,185	NOH ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Khai M. Nguyen	2819	·
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not included will be mailed in due cours	e. THIS ne initiative
1. This communication is responsive to <u>8/22/2005</u> .			
2. The allowed claim(s) is/are 1.3 and 6-8.			
3. The drawings filed on 22 January 2004 are accepted by the Examiner.			
4.			
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☐ Examiner's Amendn	e	

DETAILED ACTION

Allowable Subject Matter

1. Claims 1, 3, and 6-8 are allowed.

The following is an examiner's statement of reasons for allowance: the primary reason for allowance of the claimed invention is the inclusion of, among other things, the drawing transistor (Q4), being connected to the base of the driving transistor (Q3), for drawing a bypass current (Ic4) from the driving transistor input current to reduce the driving current in response to the input signal, and wherein the bypass current increases when the control voltage increases. The best prior art of record (the applied references in the rejection) discloses an apparatus having a bias circuit. However, the bias circuit does not have the drawing transistor, which is connected to the base of the driving transistor, for drawing a bypass current, and wherein the bypass current increases when the control voltage increases. Thus, claims 1, 3, and 6-8 are allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khai M. Nguyen whose telephone number is 571-272-1809. The examiner can normally be reached on 9:00 - 5:30 Monday-Friday.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert (Bob) J. Pascal can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

August 29, 2005

Khai M. Nguyen Art Unit: 2819 571-272-1809